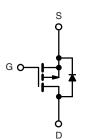


G D Top View Vishay Siliconix

Automotive P-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	- 40			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.0040			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.0060			
I _D (A)	- 120			
Configuration	Single			

TO-263



P-Channel MOSFET

FEATURES

• Halogen-free According to IEC 61249-2-21



- Package with Low Thermal Resistance
- AEC-Q101 Qualifiedd
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



FREE

ORDERING INFORMATION	
Package	TO-263
Lead (Pb)-free and Halogen-free	SQM110P04-04L-GE3

ABSOLUTE MAXIMUM RATING	S ($T_C = 25 ^{\circ}C$, unles	s otherwise noted	d)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	- 40	V	
Gate-Source Voltage		V_{GS}	V _{GS} ± 20		
Continuous Drain Current ^a	T _C = 25 °C	- I _D	- 120		
	T _C = 125 °C		- 120		
Continuous Source Current (Diode Conduction) ^a		I _S	- 120	Α	
Pulsed Drain Current ^b		I _{DM}	- 330		
Single Pulse Avalanche Current		I _{AS}	- 80		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	320	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	Б	375	W	
	T _C = 125 °C	- P _D	125	"	
Operating Junction and Storage Temperature	re Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient P	PCB Mount ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)		R _{thJC}	0.40		

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	•	•					
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = - 250 μA		- 40	-	-	.,
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$		- 2.0	- 2.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
Zero Gate Voltage Drain Current		V _{GS} = 0 V	V _{DS} = - 40 V	-	-	- 1.0	
	I _{DSS}	V _{GS} = 0 V	V _{DS} = - 40 V, T _J = 125 °C	-	-	- 50	μA
		V _{GS} = 0 V	V _{DS} = - 40 V, T _J = 175 °C	-	-	- 250	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 10 V	V _{DS} ≤ - 5 V	- 120	-	-	Α
		V _{GS} = - 10 V	I _D = - 30 A	-	0.0034	0.0040	
Drain Cauras On State Besistance	В	V _{GS} = - 10 V	I _D = - 30 A, T _J = 125 °C	-	-	0.0059	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V	I _D = - 30 A, T _J = 175 °C	-	-	0.0070	
		V _{GS} = - 4.5 V	I _D = - 20 A	-	0.0050	0.0060	
Forward Transconductanceb	9 _{fs}	V _{DS} = - 15 V, I _D = - 30 A		-	97	-	S
Dynamic ^b							
Input Capacitance	C _{iss}		V _{DS} = - 20 V, f = 1 MHz	-	11 183	13 980	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	1614	2020	
Reverse Transfer Capacitance	C _{rss}			-	1294	1620	
Total Gate Charge ^c	Qg			-	220	330	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = - 10 V	V _{DS} = - 20 V, I _D = - 110 A	-	34	-	nC
Gate-Drain Charge ^c	Q _{gd}			-	56	-	
Gate Resistance	R _g	f = 1 MHz		1.2	2.5	3.7	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	17	26	
Rise Time ^c	t _r	$V_{DD} = -20 \text{ V}, R_L = 0.18 \Omega$ $I_D \cong -110 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		-	15	23	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	112	168	
Fall Time ^c	t _f			-	45	68	
Source-Drain Diode Ratings and Chara	acteristics ^b	•					<u> </u>
Pulsed Current ^a	I _{SM}			-	-	- 330	Α
Forward Voltage	V_{SD}	I _F = - 100 A, V _{GS} = 0		-	- 0.95	- 1.5	V

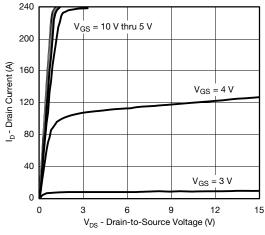
Notes

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

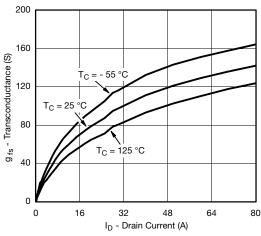
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



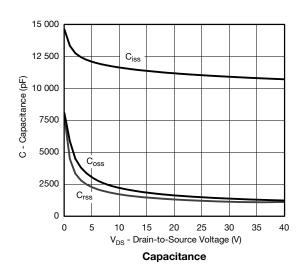
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

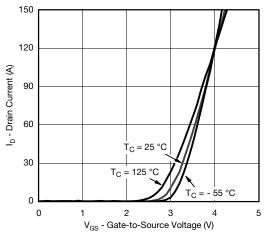


Output Characteristics

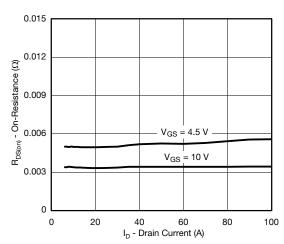


Transconductance

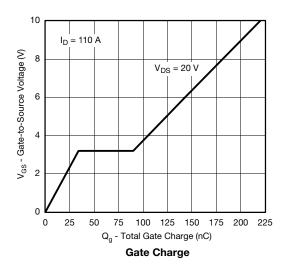




Transfer Characteristics

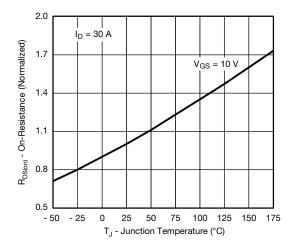


On-Resistance vs. Drain Current

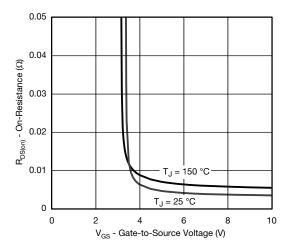




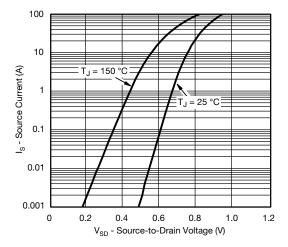
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



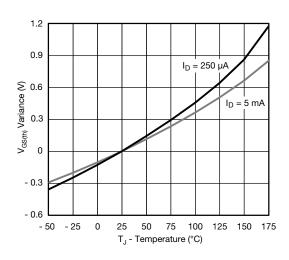
On-Resistance vs. Junction Temperature



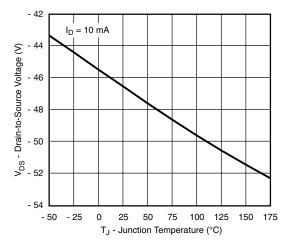
On-Resistance vs. Gate-to-Source Voltage



Source Drain Diode Forward Voltage



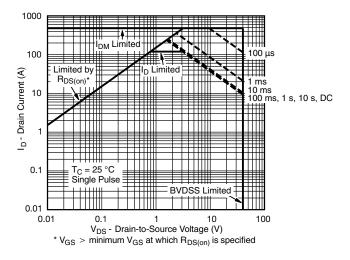
Threshold Voltage



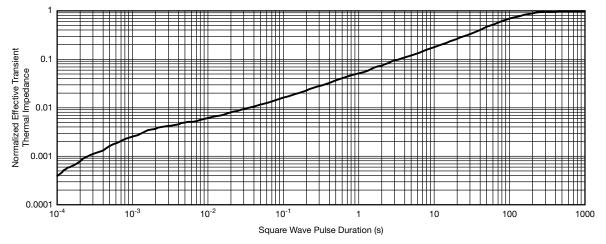
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



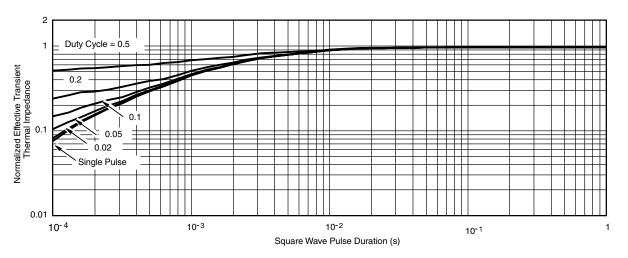
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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